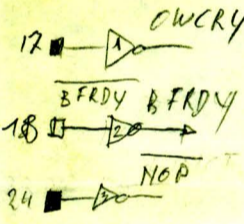
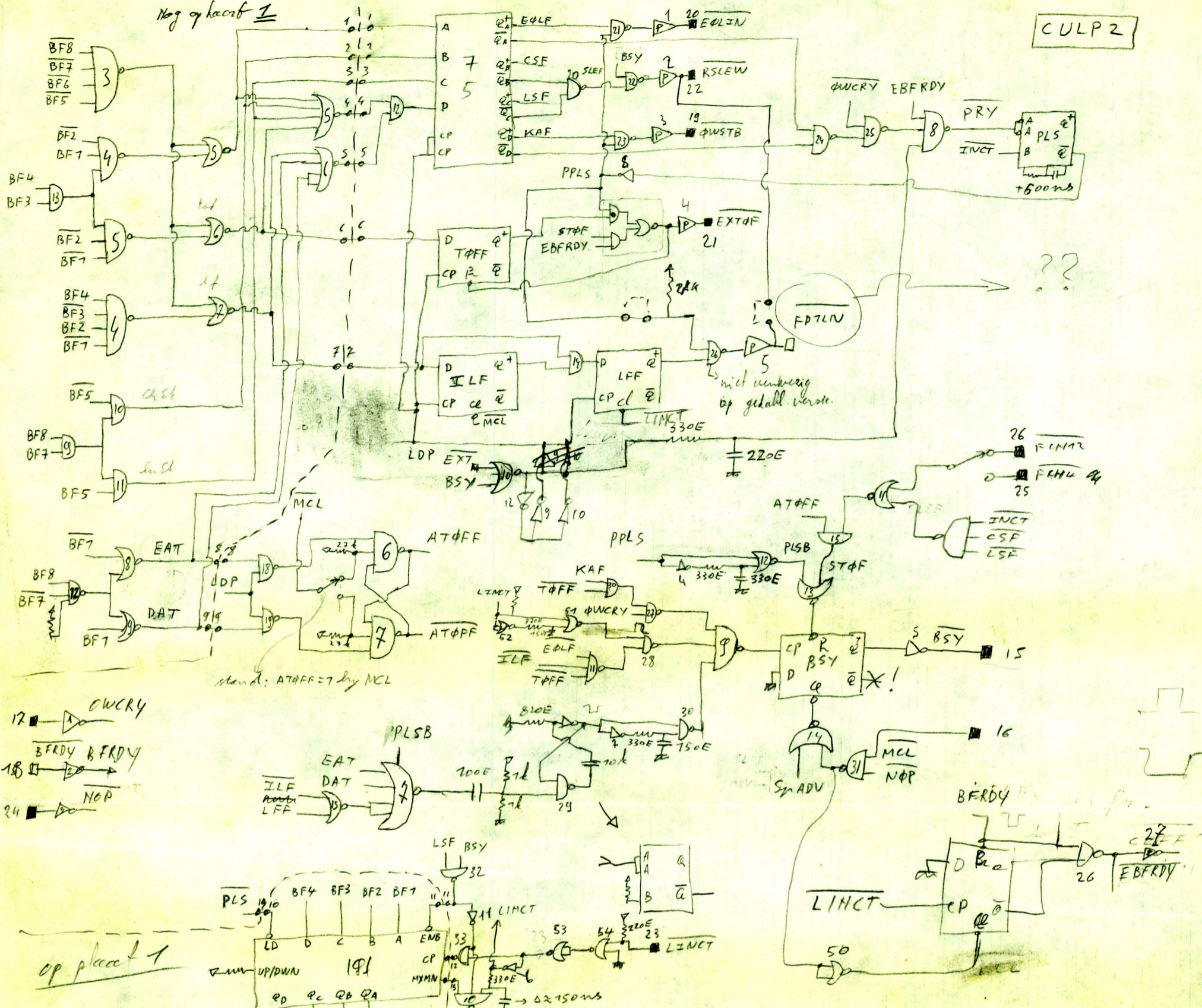
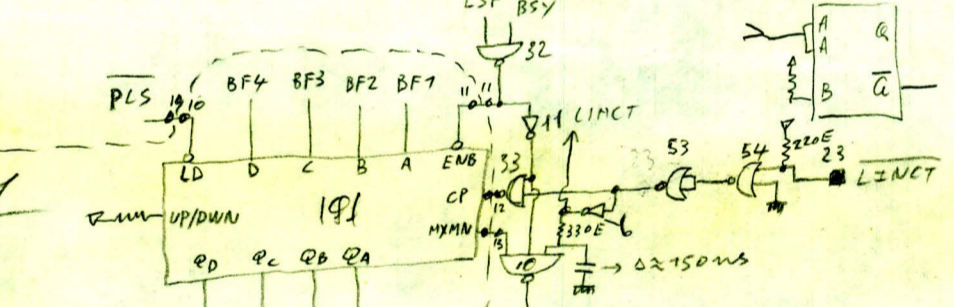


Mag op kaart 1

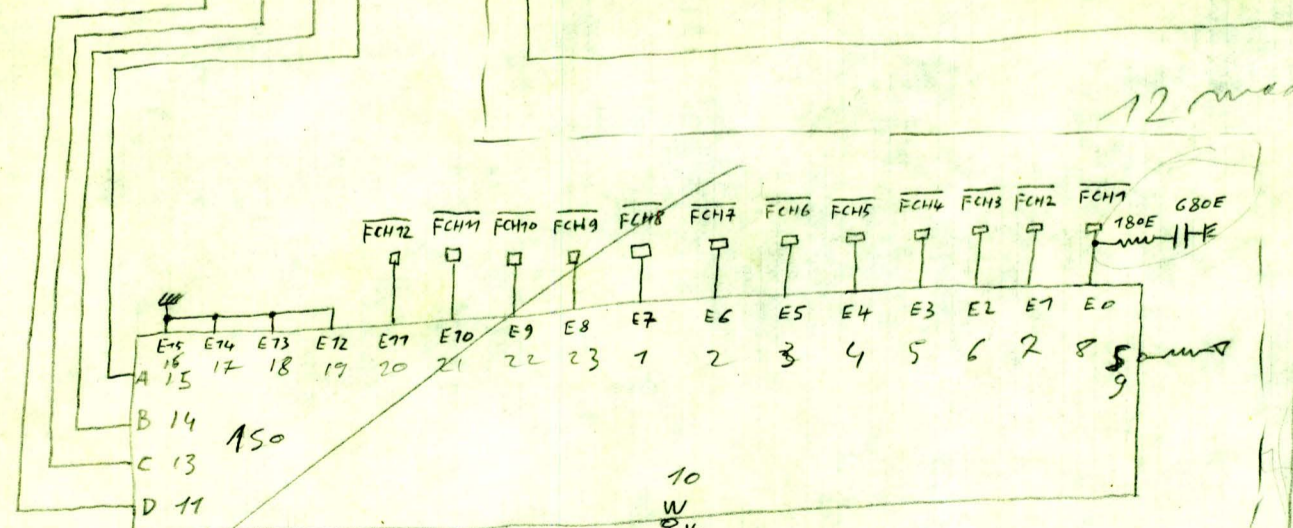
CULP 2



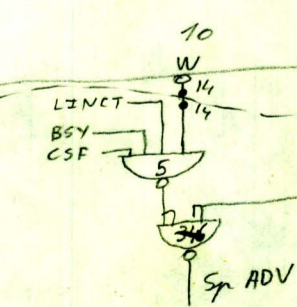
op plaat 1



12 maal



gewijzigd





INT / LP 3000 - P 860

Con Trans Elco 6041 062 000 003 (31) 3

7986	2
04	5
80	9
07	8
25	2
20	3
42	7
75	3
02	5
08	8
74	3
60	3
51	2
121	2
151	2
191	7

Elco cap / 6.3V

2

C 0.7  $\mu$ F

14

220

2

680

15

10M

1

330

3

10M sub.

3

R 3K3

8

6K8

8

1K

7

270

6

220

1

470

2

180

13

4K7

1

27K

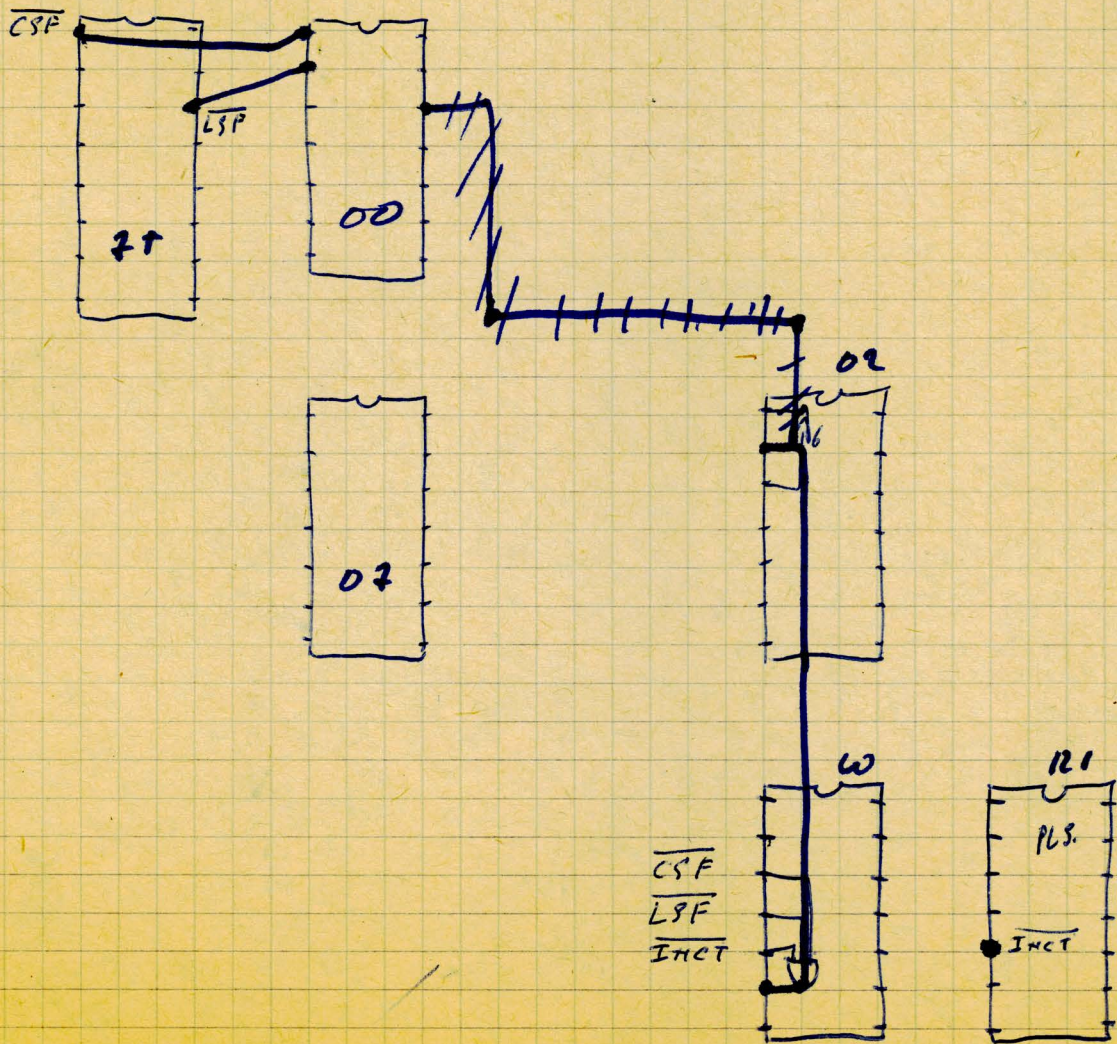
6

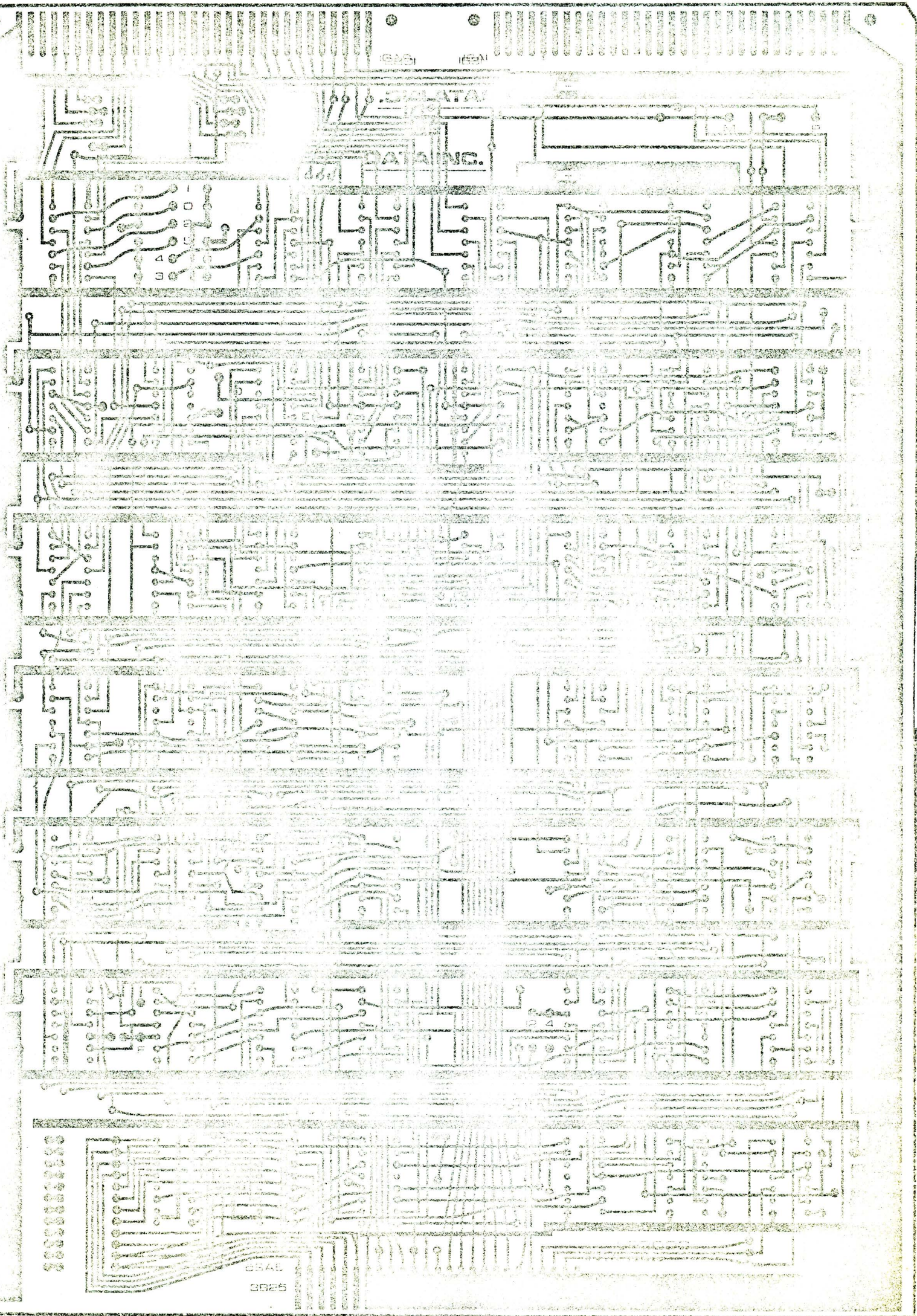
60K

9

D. BAX 13

1

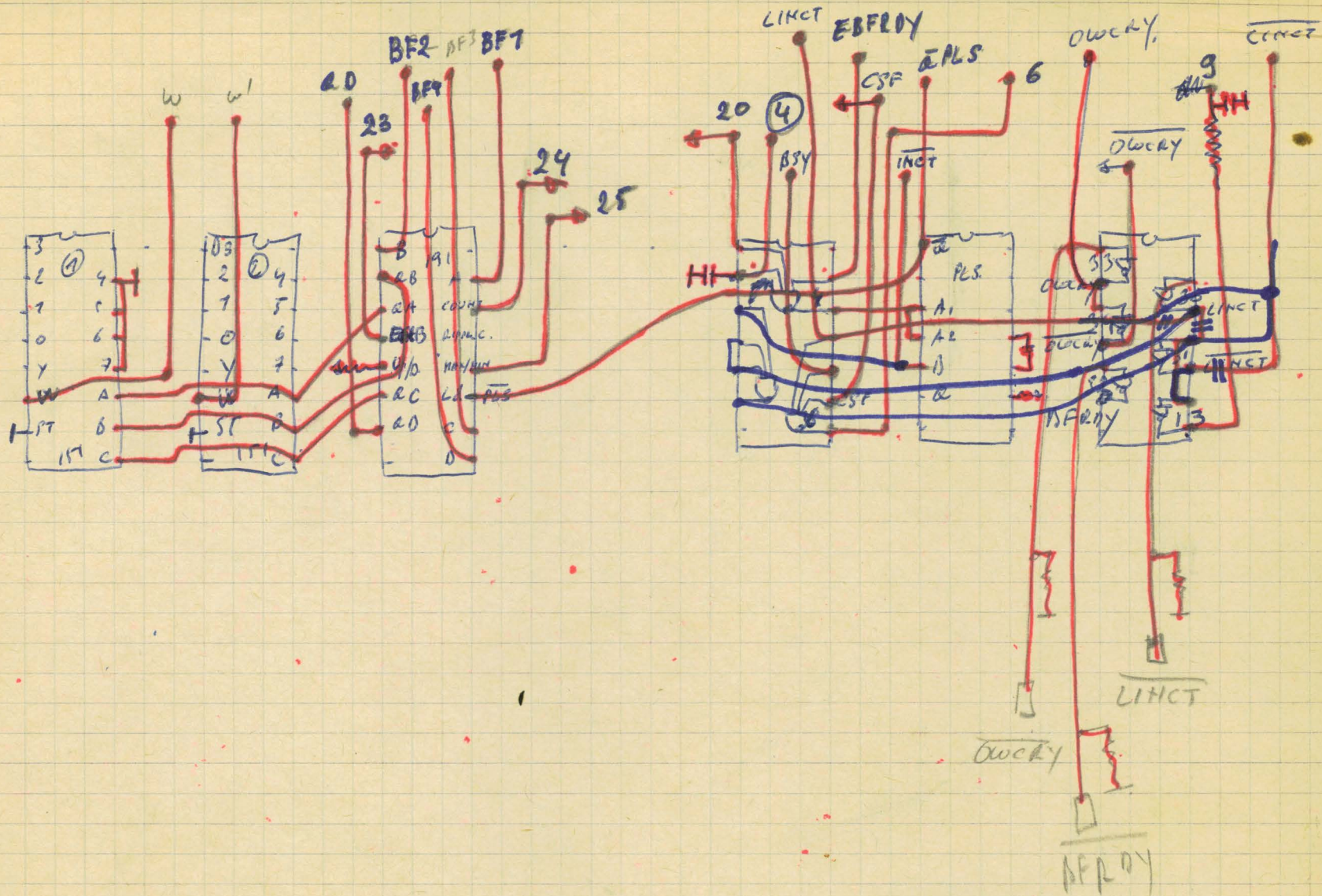




0041  
0025

DIGIT





898  
 659  
 5604



2801

1831



DATA INC.

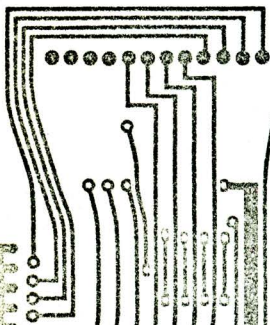
1 1  
2 2  
3 3  
4 4  
5 5

M

D

+

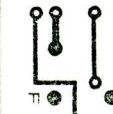
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108E

N

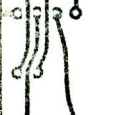
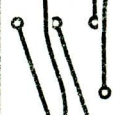
L P3000  
INT/P880



124

E

D



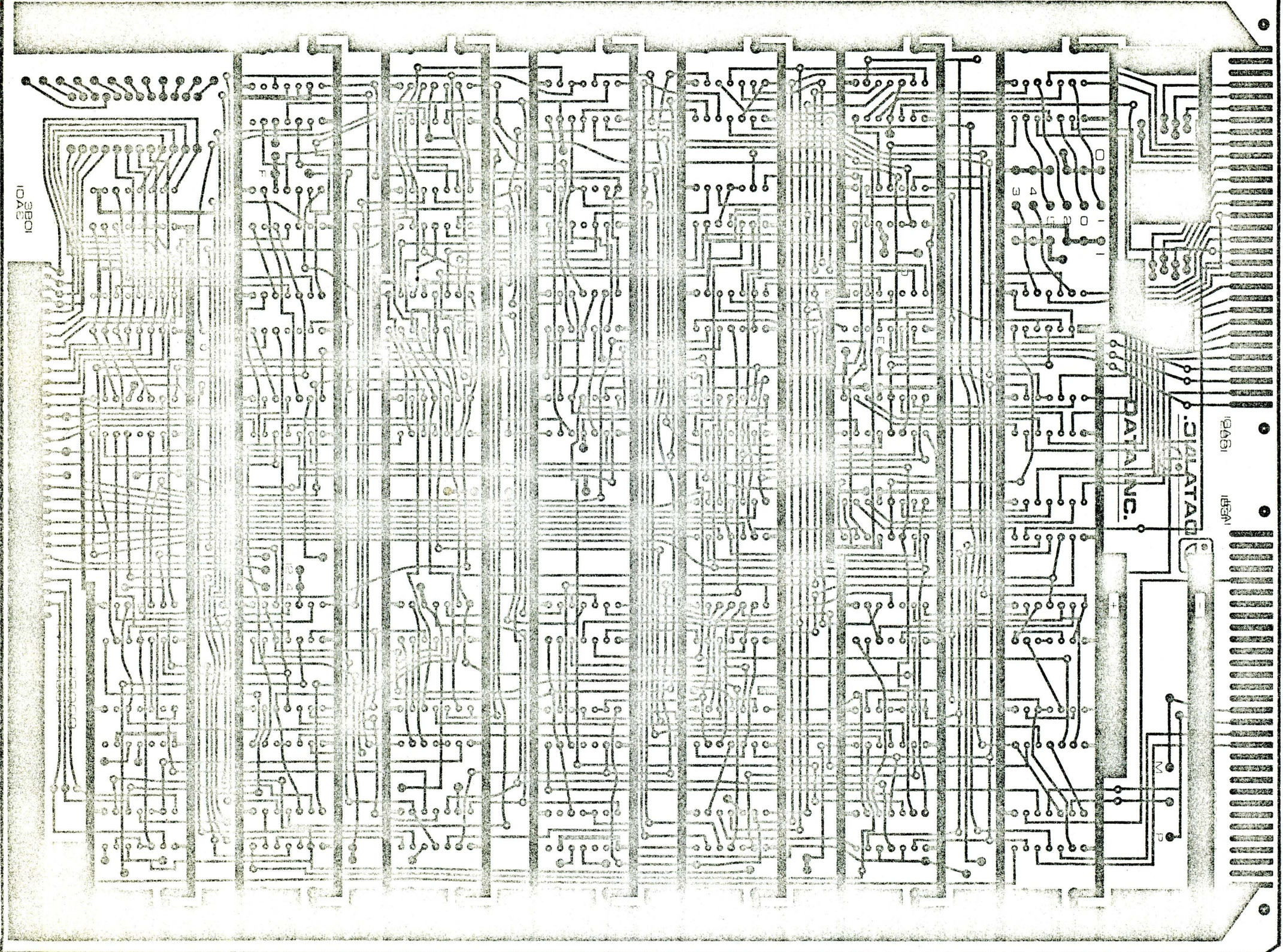
3801  
10481

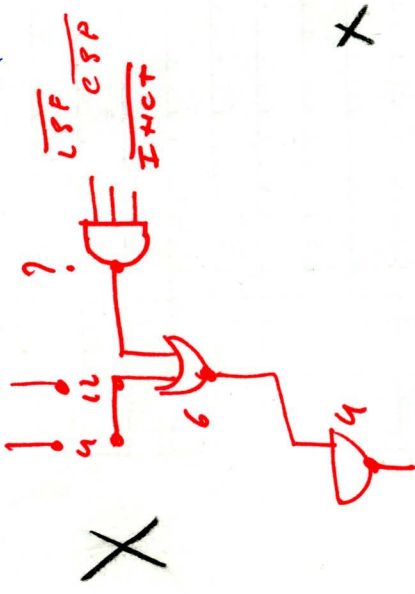
DATA INC.

10481

10481

M  
P





Diode on VCC

VCC

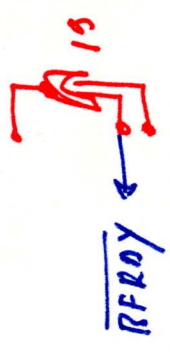
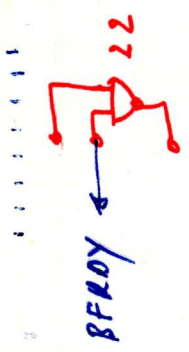
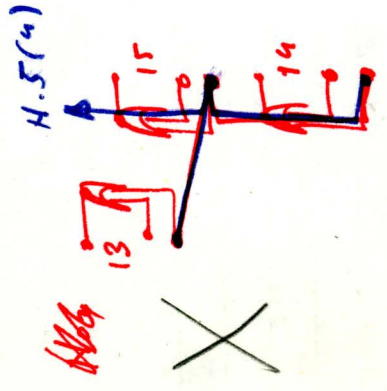
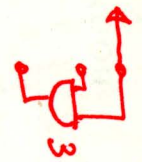
open collector gate.  $\rightarrow R = 1k$

$\overline{BF6} \rightarrow$  Verweerder  
 $a_3 \rightarrow a_3$

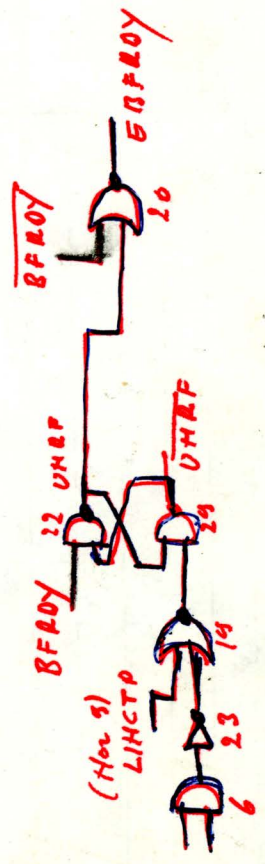
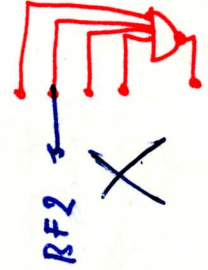
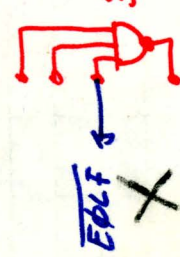
$\overline{FCH5} \rightarrow 12$  aan GND

106/100  
 2-23774  
 42238-2  
 106/100

180/100  
 582/100  
 180/100  
 582/100



~~X ILF  $\rightarrow$  CL (HCL)~~  
~~X LFF  $\rightarrow$  CL (LINCT)~~



~~X 2 x 75 Buff. a verweerder  $\rightarrow$~~

$\overline{FCH2} + \overline{FCH4}$  onloading verb.



# IHT / LPS 000 - P 960

## Connector.

3A01	<u>FCH12</u>	} <del>to</del> GND
2	<u>FCH11</u>	
3	<u>FCH10</u>	
4	<u>FCH9</u>	
5	<u>FCH7</u>	
6	<u>FCH2</u>	
7	<u>FCH3</u>	
8	<u>FCH4</u>	
9	<u>FCH8</u>	} GND
10	<u>FCH7</u>	
11	<u>FCH6</u>	
12	<u>FCH5</u>	
13	<u>EXTDF</u>	
14	<u>F07LN</u>	
15	<u>OWSTB</u>	
16	<u>EFLIN</u>	
17	<u>RSLEW</u>	
18	<u>READY</u>	
19	<u>VCCLP</u>	
20	<u>OWDT6</u>	
21	<u>OWDT5</u>	
22	<u>OWDT1</u>	
23	<u>OWDT2</u>	
24	<u>OWDT3</u>	
25	<u>OWDT4</u>	
26	<u>FCH12</u>	- GND
27	<u>FCH4</u>	
28	<u>OWERY</u>	
29	<u>BFRDY</u>	
30	<u>LINCT</u>	
31	<u>RMCLR + CLRBF</u>	

## Connector.

3B01 - 3B31 GND.



# DIGIDATA INC.

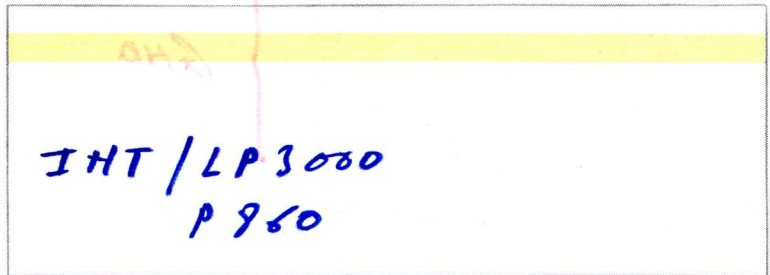
SUBSIDIARY OF  
DIGI CY. INC.

LUITENANT WILLEMOTLAAN, 80 - 9910 MARIKERKE - BELGIUM

RESEARCH - ENGINEERING - APPLICATIONS  
SPECIAL DATA EQUIPMENT

TEL. 09/26.40.41 - TELEX 11055

HR/RC	109133
TVA/BTW	408.305.761
SGB/GBM	236.130
KB	441-4580361-72
BB	G52581112



DEPARTMENT

Y/REFERENCE

O/REFERENCE

TEL-TX

9910-MARIKERKE

Con

3407

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PRINTER INPUT SIGNAL LINES

Connector Pin CPU	Signal Name	Connector Pin LP3000	Mnemonic
<i>RS</i> 3A01	Format Lines	A01-EE	FCH12
<i>ZT</i> 3A02	-	A01-DD	FCH11
<i>wt-gn</i> 3A03	-	A01-CC	FCH10
<i>OR-RD</i> 3A04	-	A01-BB	FCH09
<i>OR-BL</i> 3A05	<i>BN-RD</i> -	A01-E	FCH01
<i>gn-ZT</i> 3A06	-	A01-F	FCH02
<i>BL-OR</i> 3A07	-	A01-H	FCH03
<i>gn</i> 3A08	-	A01-K	FCH04
<i>BN</i> 3A09	-	A01-U	FCH08
<i>BN-BL</i> 3A10	-	A01-P	FCH07
<i>BL-BN</i> 3A11	-	A01-N	FCH06
<i>BN-BL</i> 3A12	-	A01-M	FCH05
<i>RS</i> 3A13	Ext Top of Form	A01-D	EXTOP
<i>gn</i> 3A14	Single Line Adv	A01-FF	FD1LN
<i>BN</i> 3A15	Data Strobe	A01-35	OWSTB
<i>ZT</i> 3A16	<i>RD-gs</i> End of Line	A01-26	EOLIN
<i>BL-BN</i> 3A17	External Slew	A01-27	RSLEW
<i>wt-gn</i> 3A18	Printer Ready	A01-NN	<del>READY</del> READY
<i>OR-RD</i> 3A19	VCC Printer	A01-03	+ 5 V
<i>gn</i> 3A20	Data Lines	A01-R	OWDT6
<i>BL-BN</i> 3A21	-	A01-S	OWDT5
<i>BL-OR</i> 3A22	-	A01-6	OWDT1
<i>gn</i> 3A23	-	A01-5	OWDT2
<i>BL-WT</i> 3A24	-	A01-J	OWDT3
<i>BN-BL</i> 3A25	<i>ZT-BN</i> -	A01-7	OWDT4
<i>gn-ZT</i> 3A26	Format Lines	A01-EE	FCH12
<i>wt-gn</i> 3A27	-	A01-K	FCH04
<i>OR-BL</i> 3A28	Character Ready	A01-8	CWCRY
<i>ZT</i> 3A29	Buffer Ready	A01-9	BFRDY
<i>R003</i> 3A30	Line Count	A01-12	LINCT
<i>BN</i> 3A31	Printer Reset	A01-31	RMCLR
	Buffer Clear	A01-37	CLRBF

3B01 --- 3B31 Grounded

A01-10

GND